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U.S. **UTILITY** Patent Application

O.I.P.E.

PATENT DATE

APPLICATION NO. CONT/PRIOR ART UNIT CLASS SUBCLASS **EXAMINER** 09/888060 438 2842

Kwon Hong Hyung-Bok Choi

Semiconductor memory device having a plug contacted to a capacitor electrode and method for fabricating the capacitor

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ORIGINAL		CROSS REFERENCE(S)
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TERMINAL		DRAWINGS		CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent			•	NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant I	Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date				,	x .	
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